

ABSTRACT OF THE DISCLOSURE

A semiconductor device capable of stably capturing and holding data is obtained. This semiconductor device comprises a data input part, a control part supplying a synchronous signal for capturing a data signal, a data capturing part for capturing the data signal having a prescribed amplitude and determining the data signal while level-converting the data signal of the prescribed amplitude to an amplitude different from the prescribed amplitude in response to the synchronous signal from the control part and a latch part provided independently of the data capturing part for holding the data signal captured in the data capturing part. The data capturing part is substantially connected to a power source at least when capturing the data signal and determining the data signal. Thus, the circuit operation is not unstabilized when capturing the data signal and determining the data signal, whereby the data signal can be stably captured and determined. The latch part is provided independently of the data capturing part, whereby the data signal captured by the data capturing part is stably held.